Apple Lab

The Apple Lab has 60 computers with Turbo C,C++ compilers and MS office packages. This Lab for all Semester students to practice programming in C language by implementing  C control structures , pointers, array operations, etc. This lab is equipped with Mac book with ios operating systems. Students have the choice to work on mac based operating systems and as well as windows based operating systems.

C Programming Lab

C Programming Lab is equipped with 60 networked PCs. The software used in this lab are C++,C. This lab is meant for better understanding of  the data structure concepts. This lab has machines running on LINUX and Windows.

Sun Solaris Lab

Sun Solaris Lab consists of 35 LAN connected machines running on LINUX and Windows. Sun Solaris Lab is used for coding and programming.

Digital Logic Lab

The digital logic lab consists of number of digital logic trainers which uses various integrated circuits of various gates for hardware implementation. This lab gives knowledge of electronics and communication.

Research lab

In more details, our Research Lab is responsible for research on base technologies comprising of high-performance computing, cloud, language·voice·visual intelligent, and big data handling. This lab is equipped with latest technology machines to give highly accurate results.

Microprocessor lab

This laboratory is used to provide intensive practical exposure to the students in the field of microprocessor architecture and industrial control through them. Different exercises in this lab includes Serial Data Communication between PC and 8085 microprocessor trainer kit, writing on EPROM and microcontroller Chip using UIP. The lab also provides the facility to interface the microprocessor with different circuits such as A/D converters, stepper motors, DC motors, multidigit displays, etc.  This lab has various types of microprocessor, micro controller trainer kits along with interfacing modules to demonstrate the detailed applications of microprocessors. The purpose of this laboratory is to train the students to be familiar with the software and hardware of microprocessors so that they can gain enough experiences to meet the demand of the microprocessor era.